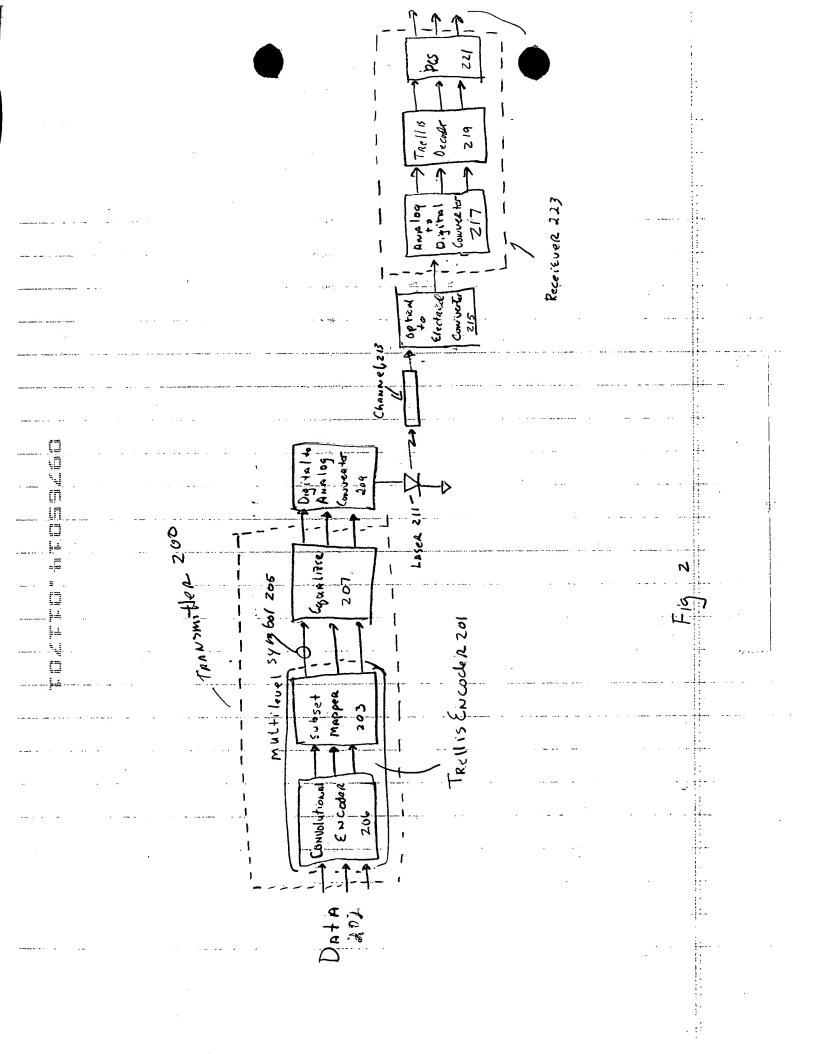


Figure 1

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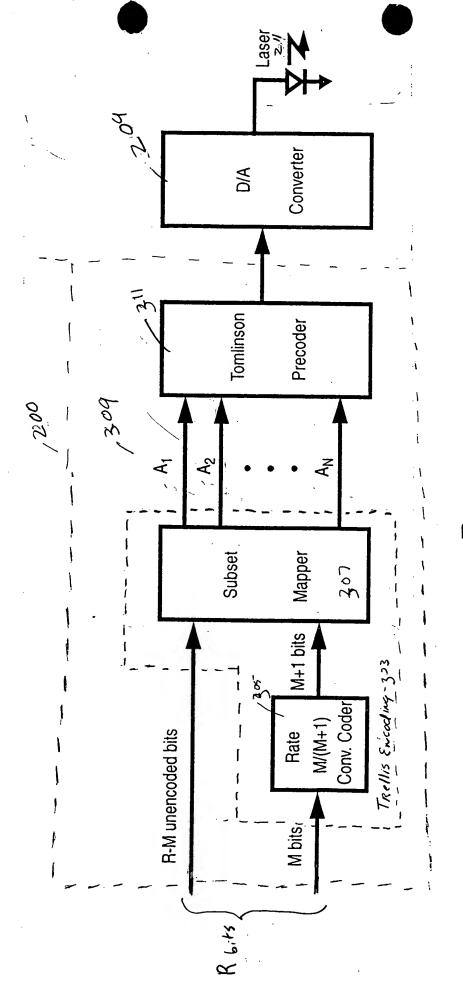
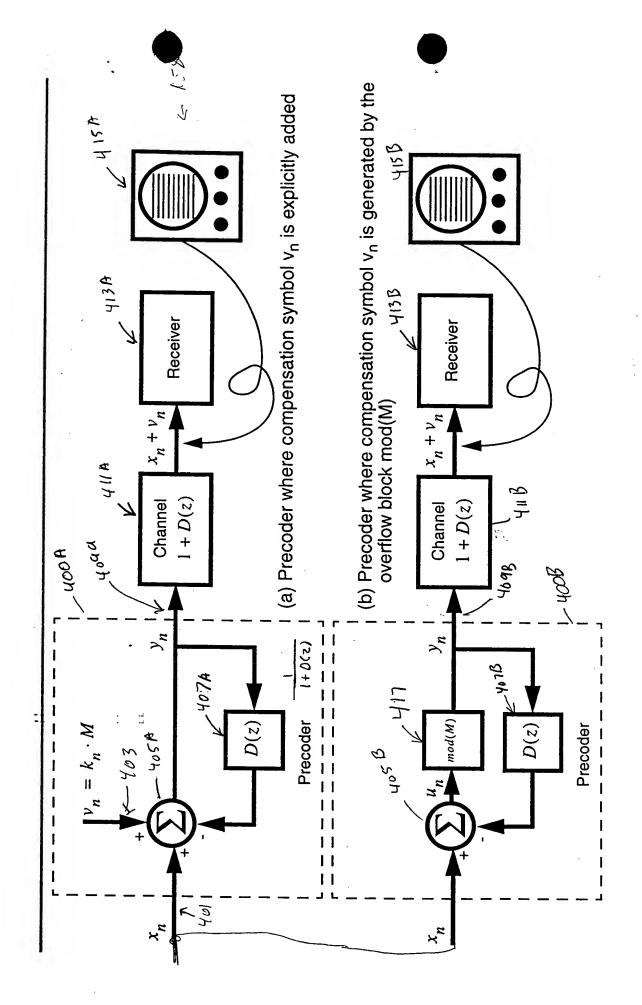


Figure 3

## Tomlinson-Harashima Precoding (THP)



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Fig 4a.

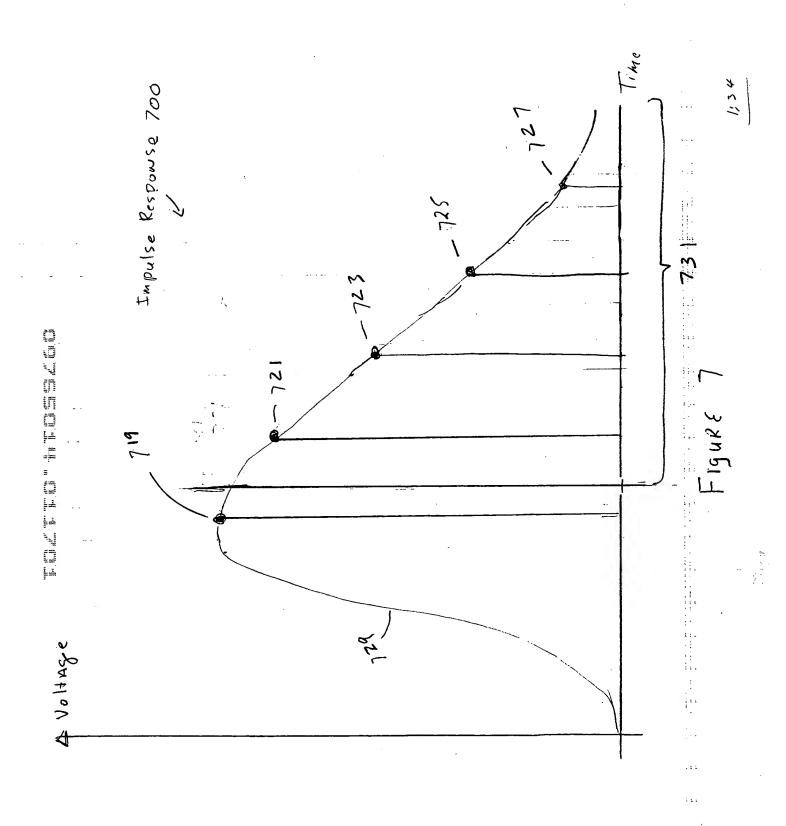
System Interface 523 251 PCS N-Dimensional Trellis Decoder ္ပ္ 519 513 Timing Recovery 515 <u>့ပ</u> RETIMING Fig wee 5 8 8 8. 8 <u>8</u> 805 207 305. 503 Photodetector

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FiguRE 6

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## Channel Model

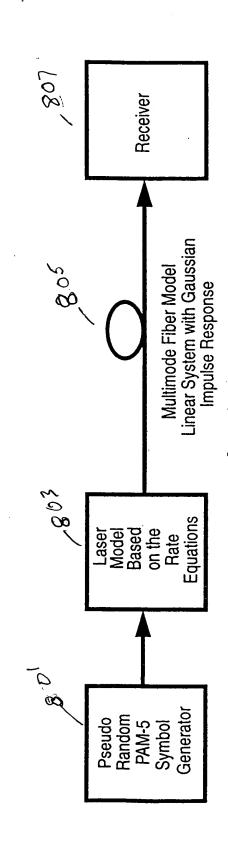
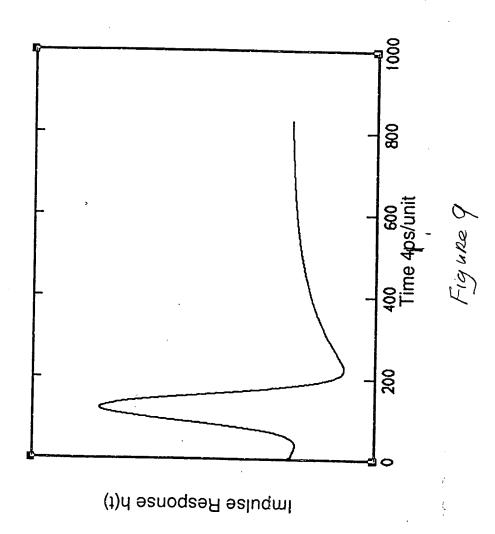
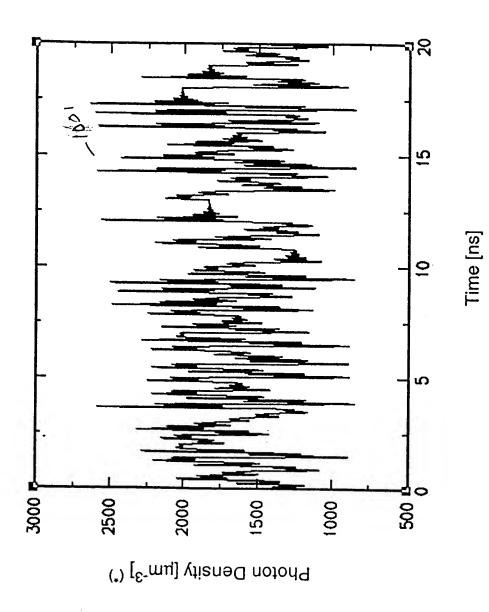


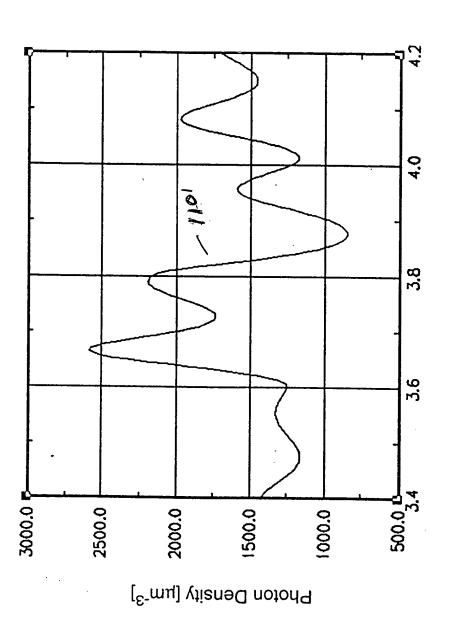
Figure 8



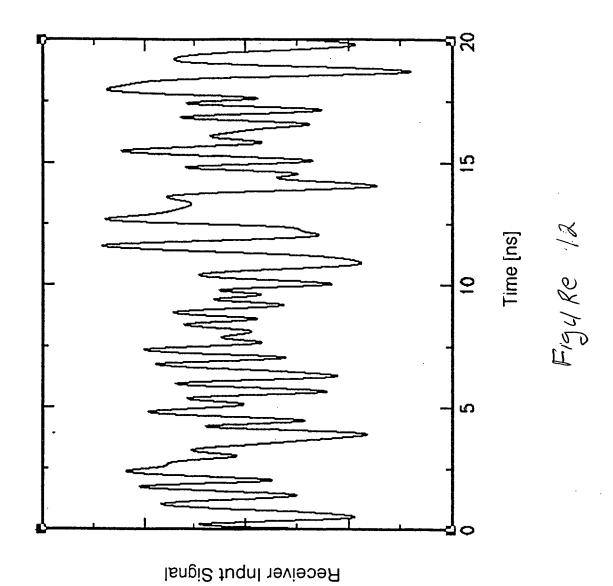


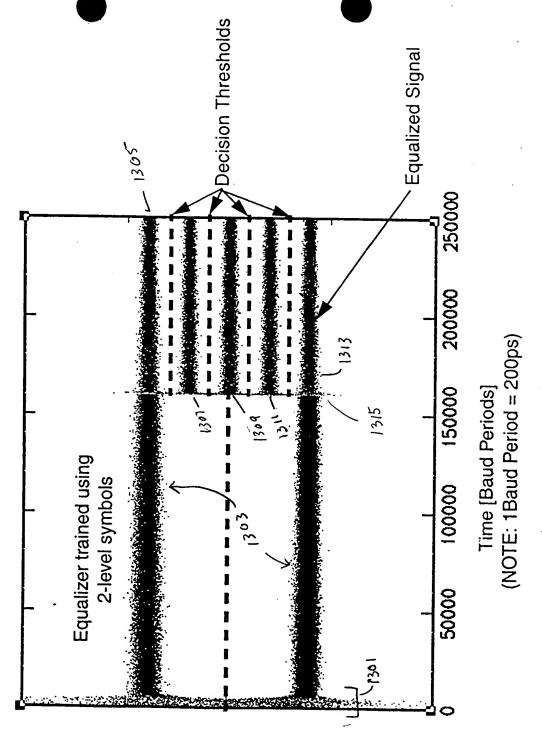
(\*) Consistent with a bias current of 60mA

Figure to

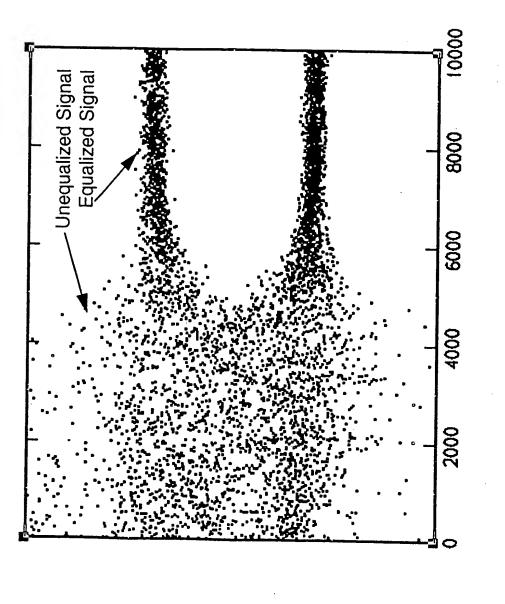


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